

Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification

By Zainalabedin Navabi

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This rigorous text shows electronics designers and students how to deploy Verilog in sophisticated digital systems design.The Second Edition is completely updated -- along with the many worked examples -- for Verilog 2001, new synthesis standards and coverage of the new OVI verification library.

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Editorial Review

From the Author
The book comes with a CD that contains Verilog and VHDL simulators, lecture material, and a web based electronic indexing tool that we call Verilog Circuit Navigator. To see how the navigator works, please send email to navabi@ece.neu.edu for a demo version.

From the Back Cover
LEARN VERILOG DESIGN WITH A MASTER

This rigorous tutorial shows electronics designers and students how to apply Verilog in sophisticated digital systems design, using over a hundred skill-building, fully worked-out, and simulated examples. Completely updated, the second edition covers Verilog 2001, new synthesis standards, testing and testbench developments, and the new OVL verification library. You'll find out just what's involved in using Verilog hardware description language (HDL) in digital system design. HDL expert Zain Navabi explains the design process in logical sequence -- the way it's done in the real world.

Moving from simple concepts to the more complex, Navabi interprets Verilog constructs related to design stages and design abstractions, including behavioral description, dataflow description, and structure description. With emphasis on the concepts of concurrency and delay in hardware, the text helps you grasp the essence of HDLs. Clear specification of learning objectives at the beginning of each chapter and end-of-chapter problems focus attention on key concepts.

If mastery of design with Verilog is the goal, Zain Navabi's ***Verilog Digital System Design*** is the tool.

MUST-HAVE CD INCLUDED:

- Verilog and VHDL simulators
- Synthesis tools
- Mixed-level logic and Verilog design environment
- FPGA design tools and environments from Altera
- Related tutorials and standards
- All worked examples from the book, including testbench and simulation-run reports for every example
- Complete CPU examples with Verilog code and software tools
- OVL verification libraries and tutorials

THE BEST VERILOG TUTORIAL -- UPDATED:

- Verilog 2001, step by step in examples and text
- OVL verification library
- New synthesis standards
- New chapter on testbench development and verification
- Problem set in each chapter encourages review and test of key concepts
- Instructor's manual and lecture slides available for class use

About the Author

Zainalabedin Navabi (Boston, MA) is Professor of Electrical and Computer Engineering at Northeastern University. He is the author of *Verilog Digital System Design.*

Users Review

From reader reviews:

Robert Hester:

Often the book Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification has a lot of information on it. So when you make sure to read this book you can get a lot of profit. The book was authored by the very famous author. This articles author makes some research previous to write this book. This book very easy to read you can get the point easily after perusing this book.

Brenda Robert:

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Judy Washburn:

The book untitled Verilog Digital System Design: Register Transfer Level Synthesis, Testbench, and Verification contain a lot of information on that. The writer explains the girl idea with easy approach. The language is very straightforward all the people, so do not necessarily worry, you can easy to read the item. The book was compiled by famous author. The author will take you in the new time of literary works. You can actually read this book because you can please read on your smart phone, or program, so you can read the book in anywhere and anytime. If you want to buy the e-book, you can wide open their official web-site as well as order it. Have a nice examine.

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